





Attendance. A student missing a lab must work out a time with the instructor to make it up. Missing more than 2 labs will result in a failing grade in the course.

Lab teams. The students will be divided into teams of two or three individuals. **The lab work, lab notebook, and technical reports are the responsibility of all team members.**

Course website. Lab procedures, dates, and course announcements will be distributed to the class via the course website. It is the responsibility of each student to **check this website regularly.**

Lab decorum. Each lab team member is expected to earnestly participate in each experiment. **Students must respect the lab equipment and clean their lab bench** by properly stowing all wires and components after each experiment. Food and drink are not permitted at any time in the laboratories. Each student must understand and follow all safety instructions. Horseplay is strictly prohibited at all times and could result in the removal of students from the course. All potentially hazardous situations must be reported to the instructor.

Accommodations for learning disabilities. Upon receipt of a bona fide letter from the Office of Access Services, Instruction, and Support (OASIS), appropriate accommodations will be made for learning disabilities. However, nothing can be done retroactively.

Lab notebooks. Each lab team will keep a handwritten lab notebook in accordance with the prescribed format. The lab notebooks must be available at each lab meeting.

Pre-lab exercises. **Each lab team will complete the “Pre-lab” exercises for each lab before arriving to perform the lab experiment.** These “Pre-lab” portions will be checked by your instructor at the beginning of each lab period. Components and wires required to construct each circuit for each pre-lab will be available in the lab room (GRIMS 311) ahead of each lab.

Lab reports. Lab reports must follow the prescribed format and be the printed output of a word processor. **Lab reports must be submitted at the beginning of the lab period** on the due date.

Lab practical. At the end of the course, each student will demonstrate *individually* to the instructor the ability to construct circuits from a schematic diagram, to operate the test equipment, and to take and interpret data. This lab practical examination will take place during the last regular class period and will be given in lieu of a final examination.

<i>Week</i>	<i>Due at start of class</i>	<i>Activity</i>
1		Lab orientation
2	Pre-lab #1	Lab #1, Amplifier models
3	Pre-lab #2, Report #1	Lab #2, Diode characterization
4	Pre-lab #3, Report #2	Lab #3, Diode circuits
5	Report #3	Lab #4a, DC Motor Driver Kit
6	(completion of Lab #4a)	Lab #4b, DC Motor Driver Lab
7	Pre-lab #5, Report #4	Lab #5, CMOS circuits
8	Pre-lab #6, Report #5	Lab #6, MOSFET characterization
9	Pre-lab #7, Report #6	Lab #7, MOSFET amplifier configurations
10	Pre-lab #8, Report #7	Lab #8, BJT characterization
11	Pre-lab #9, Report #8	Lab #9, Common-emitter transistor amplifier
12	Notebooks, Report #9	Lab practical (exam)